

**IN THE SPECIFICATION**

Please replace the second full paragraph of page 16 with the following paragraph:

The invention is most efficiently used with synchronous architecture, an example of which is disclosed in co-pending application Serial No. 09/653,394, assigned to the assignee of this invention. In a synchronous transmitter, each component derives its timing signal from a single master clock, either directly or through simple multiplier or divider means. In a corresponding synchronous receiver, the cross-coupling circuitry is greatly simplified, because each synchronization loop derives its timing signal from the master clock either directly or through simple multiplier or divider means.